

The opinion in support of the decision being entered today is *not* binding precedent of the Board.

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE BOARD OF PATENT APPEALS
AND INTERFERENCES

Ex parte JOACHIM ARLT and KARL-HERMANN BUSSE

Appeal 2007-2501
Application 10/026,917¹
Technology Center 2800

Decided: August 24, 2007

Before ADRIENE LEPIANE HANLON, RICHARD TORCZON, and
SALLY C. MEDLEY, *Administrative Patent Judges*.

MEDLEY, *Administrative Patent Judge*.

DECISION ON APPEAL

1 **A. Statement of the Case**

2 Applicants appeal under 35 U.S.C. § 134 from a final rejection of
3 claims 1-4, 6-9, 11, 12, 14, and 15. We have jurisdiction under 35 U.S.C.
4 § 6(b).

1 Application for patent filed 21 December 2001. The real party in interest is VenTec Gesellschaft für Venturekapital und Unternehmensberatung.

1 The prior art relied upon by the Examiner in rejecting the claims on
2 appeal is:

3	Morita	US 5,815,366	Sept. 29, 1998
4	Brown	US 5,948,986	Sept. 7, 1999
5	Hwang	US 6,238,160	May 29, 2001
6	Wytman	US 6,354,791	Mar. 12, 2002
7	O'Mara	US 6,444,033	Sept. 3, 2002

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9 Claims 1, 6, 7, 9, 11 and 15 stand rejected under 35 U.S.C. § 102(e) as
10 being anticipated by Hwang (Final Rejection 2 and Answer 3²).

11 Claim 2 stands rejected under 35 U.S.C. § 103(a) as being
12 unpatentable over Hwang in view of O'Mara (Final Rejection 4 and Answer
13 4).

14 Claim 3 stands rejected under 35 U.S.C. § 103(a) as being
15 unpatentable over Hwang in view of Wytman (Final Rejection 4 and Answer
16 4).

17 Claims 4 and 12 stand rejected under 35 U.S.C. § 103(a) as being
18 unpatentable over Hwang in view of Morita (Final Rejection 5 and Answer
19 5).

20 Claims 8 and 14 stand rejected under 35 U.S.C. § 103(a) as being
21 unpatentable over Hwang in view of Brown (Final Rejection 6 and Answer
22 6).

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2 In the Answer, we assume claim 11 was omitted in error, since claim 15

1 BACKGROUND

2 The invention is related to a method and system for manipulating
3 semiconductor substrates. A semiconductor substrate is placed on a
4 transportable electrostatic carrier (chuck) (Specification 13:7-8). The
5 substrate remains on the transportable electrostatic chuck during at least two
6 processing steps (Specification 13:11-14). Both the transportable
7 electrostatic chuck and the substrate are moved through the various
8 processing steps (Specification 14:2-4). The transportable electrostatic chuck
9 is charged beforehand and does not have any additional power supply
10 connected to it during the processing steps to allow the chuck to be moved to
11 the different locations (Specification 12:12-15 and 13:15-18).

12 **B. Issue**

13 The issue is whether Applicants have shown that the Examiner erred in
14 determining claims 1-4, 6-9, 11, 12, 14, and 15 to be unpatentable over the
15 prior art as applied by the Examiner.

16 **C. Findings of fact (“FF”)**

17 The record supports the following findings of fact as well as any other
18 findings of fact set forth in this opinion by at least a preponderance of the
19 evidence.

20 1. Applicants’ claims 1-4, 6-9, 11, 12, 14, and 15 are the subject of
21 this appeal.

which depends from claim 11 stands rejected.

1 2. The Examiner finally rejected independent claim 1 and
2 independent claim 11, the only independent claims, as being anticipated by
3 Hwang.

4 3. Claim 1 is as follows:

5 1. A method of manipulating semiconductor substrates comprising
6 placing a semiconductor substrate on a transportable electrostatic
7 chuck, and keeping the semiconductor substrate clamped on the
8 electrostatic chuck for the duration of and between at least two
9 processing steps of the semiconductor substrate without any additional
10 external power supply to recharge the transportable electrostatic chuck
11 during long or several process steps or operation steps.
12

13 4. Claim 11 is as follows:

14 11. An electrostatic carrier system for manipulating semiconductor
15 substrates, the system comprising at least one transportable
16 electrostatic chuck for a semiconductor substrate and at least one
17 transfer station for transferring the transportable electrostatic chuck
18 with the semiconductor substrate placed thereon between processing
19 steps, the electrostatic chuck being configured so as to clamp the
20 substrate without any additional external power supply to recharge the
21 transportable electrostatic chuck during long or several process or
22 operation steps.
23
24

1 Hwang

2 5. The Examiner found that Hwang describes placing a semiconductor
3 substrate **26** on a transportable electrostatic carrier item (chuck) **38**. (Final
4 Rejection 2 and Answer 3).

5 6. The Examiner further found that the semiconductor substrate **26**
6 remains on the chuck **38** during processing steps that include moving the arm
7 and wafer and then rapidly moving the arm into the process chamber. (Final
8 Rejection 2-3 and Answer 3).

9 7. The Examiner also found that the processing steps of moving the
10 arm and wafer and then rapidly moving the arm into the process chamber are
11 performed under a single power application and “without any additional
12 external power supply to recharge the transportable electrostatic chuck during
13 long or several process steps. . . .” (Answer 3:10).

14 8. Hwang describes an arm **20** (**Fig. 1**) used for transferring a
15 semiconductor substrate **26** into a chamber **54** (**Fig. 5**).

16 9. Concentrically disposed within contact member **34** of the arm **20** is
17 a plate **38** (**Fig. 2**) (Hwang 4:41-48).

18 10. A power source **28** is coupled with the arm **20** through electrical
19 leads **32** (Hwang 4:56-58).

20 11. One of the leads **32** is connected with contacting member **34** and
21 the other lead is connected to plate member **38** (Hwang 4:59-62 and **Fig. 1**).

1 12. During the steps of moving the arm **20** (and wafer on top of plate
2 **38**), the power supply remains on and connected so that a charge is
3 maintained to keep the substrate chucked to plate **38** (Hwang 5:28-46).

4 **D. Principles of Law**

5 35 U.S.C. § 102

6 “A person shall be entitled to a patent unless....the invention was
7 patented or described in a printed publication in this or a foreign country or
8 in public use or on sale in this country, more than one year prior to the date
9 of the application for patent in the United States” 35 U.S.C § 102(b).

10 To anticipate a claim, a prior art reference must disclose every
11 limitation of the claimed invention, either expressly or inherently. *Verdegaal*
12 *Bros. Inc., v. Union Oil Co.*, 814 F.2d 628, 631, 2 USPQ2d 1051, 1053 (Fed.
13 Cir. 1987).

14 **E. Analysis**

15 The Examiner finally rejected independent claims 1 and 11 and
16 dependent claims 6, 7, 9, and 15 as being anticipated under 35 U.S.C.
17 § 102(e) by Hwang. The Examiner relied on Hwang to teach “without any
18 additional external power supply to recharge the transportable electrostatic
19 chuck during long or several process steps or operation steps,” recited in both
20 claim 1 and claim 11. The Examiner indicated that the steps of moving the
21 arm **20** and rapidly moving the arm to the process chamber are performed
22 under a single power application and meet the limitation (FF 7).

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1 Applicants argue that Hwang discloses a permanent external voltage supply,
2 but in the claimed invention there is no additional external power supply (Br.
3 6). In response, the Examiner failed to specifically address Applicants'
4 argument that Hwang describes the opposite of what is claimed.

5 In Hwang, one of the leads **32** is connected with contacting member **34**
6 and the other lead is connected to plate member **38** (FF 11). During the steps
7 of moving the arm (and wafer on top of plate **38**), the power supply remains
8 connected and on through leads **32** to maintain the charge to plate **38** (FF 12).

9 Based on the record, the Examiner has failed to sufficiently
10 demonstrate that Hwang describes the limitation "without any additional
11 external power supply to recharge the transportable electrostatic chuck during
12 long or several process steps or operation steps."

13 Therefore, we cannot sustain the rejection of the claims 1, 6, 7, 9 11
14 and 15. As applied by the Examiner, none of Morita, Brown, Wytman, or
15 O'Mara makes up for the deficiencies of Hwang.

16 **E. Decision**

17 Upon consideration of the record, and for the reasons given, the
18 Examiner's rejections are reversed.

19 The Examiner's rejection of claims 1, 6, 7, 9, 11 and 15 under 35
20 U.S.C. § 102(e) as being anticipated by Hwang is reversed.

21 The Examiner's rejection of claim 2 under 35 U.S.C. § 103(a) as being
22 unpatentable over Hwang in view of O'Mara is reversed.

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1 The Examiner's rejection of claim 3 under 35 U.S.C. § 103(a) as being
2 unpatentable over Hwang in view of Wytman is reversed.

3 The Examiner's rejection of claims 4 and 12 under 35 U.S.C. § 103(a)
4 as being unpatentable over Hwang in view of Morita is reversed.

5 The Examiner's rejection of claims 8 and 14 under 35 U.S.C. § 103(a)
6 as being unpatentable over Hwang in view of Brown is reversed.

REVERSED

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